

PATENT APPLICATION

Sheet 1 of 1

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
	200309887-1		
	APPLICANT David Christpher Burden FILING DATE Herewith	GROUP	

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
ABP	1A 2003/0028816	02/06/2003	Bacon	
	1B			
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	1D			
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FOREIGN PATENT DOCUMENTS

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		1L				
		1M				
		1N				
		1O				
		1P				

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

ABP	1Q	On-Line Scheduling of Hard Real-Time Tasks on Variable Voltage Processor; Inki Hong, Miodrag Potkonjak and Mani B. Srivastava; Computer Science Dept., Univ. of CA, LA, CA; and Electrical Engineering Dept., Univ. of CA, LA, CA
ABP	1R	PASA: A Software Architecture For Building Power Aware Embedded Systems; Cristiano Pereira and Rajesh Gupta; Dept. of Information and Computer Science, University of California, Irvine; and Mani Srivastava; Dept. of Electrical Engineering, University of CA, Los Angeles
ABP	1S	A Power-Aware, Best-Effort Real-Time Task Scheduling Algorithm; Jinggang Wang, Binoy Ravindran, and Tom Martin; The Bradley Dept. of Electrical and Computer Engineering, Virginia Tech, Blacksburg, VA

EXAMINER	/Anand Patel/	DATE CONSIDERED	08/24/2006
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APP	1Q	20.4 A 0.9V to 1.95V Dynamic Voltage-Scalable and Frequency-Scalable 32b PowerPC Processor; Nowka, Carpenter, MacDonald, Ngo, Brock, Ishii, Nguyen & Burns; IBM Austin Research Lab; IBM Microelectronics Division, Austin, TX; IBM Microelectronics Div., Yasu, Japan; ISSCC 2002 / Session 20 / Microprocessors / 20.4; 2002 IEEE International Solid-State Circuits Conference; 0-7803-7335-9
APP	1R	Power-Aware Scheduling for Embedded Systems under Min/Max Power and Timing Constraints; Liu, Chou, & Bagherzadeh; Dept. of Electrical & Computer Engineering; University of California, Irvine; and Aranki, & Toomarian; California Institute of Technology; Jet Propulsion Laboratory, Pasadena, CA
APP	1S	Energy Aware Scheduling for Distributed Real-Time Systems; Misshra, Rastogi, Zhu, Mosse & Melhem; Computer Science Dept., University of Pittsburgh, Pittsburgh, PA

EXAMINER /Anand Patel/	DATE CONSIDERED 08/24/2006
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